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LM-EAMDR-01

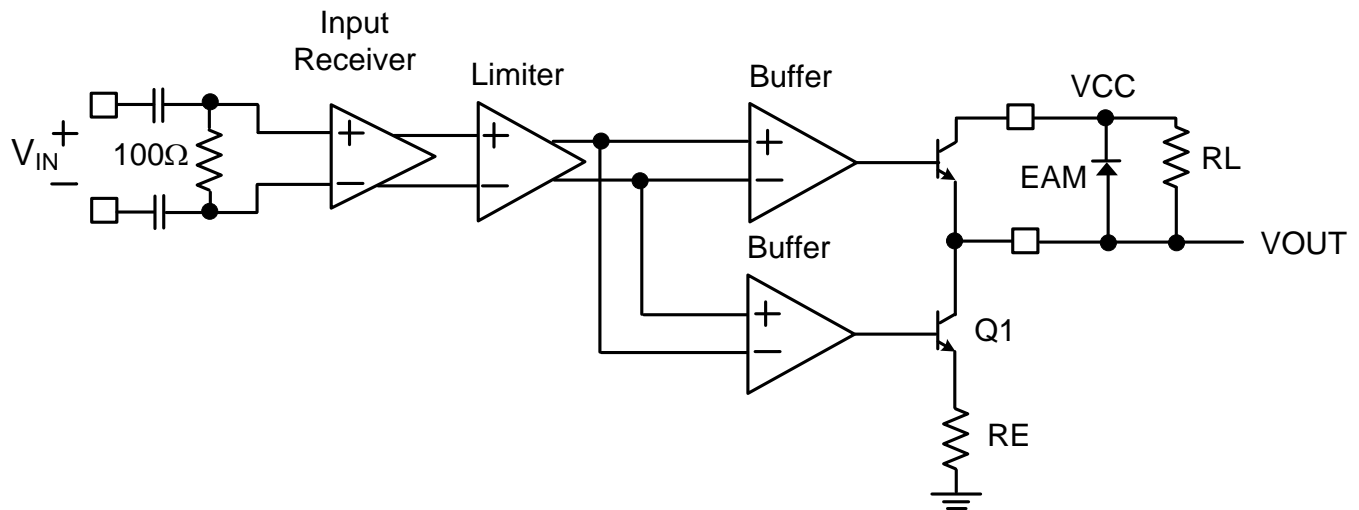
28 Gbps, Low Power EAM Driver IP Block

Introduction

This document describes the features and key specifications of a High Speed, Low Power, Electro-Absorption Modulator Driver IP block. The process technology for this block is 180 nm SiGe. Modifications to this circuit and different process options are available. Please contact Linear MicroSystems at sales@linearmicrosystems.com for a quotation today.

Features:

- 28 Gbps operation
- 4 channel version supports 100 GBps
- Adjustable output swing: 1.5 – 2.0V peak-to-peak single-ended
- Rise and fall times < 15 ps
- Class AB operation saves DC power consumption
- 78 mW average power per channel for 2V swing, $R_L = 100\Omega$
- $V_{CC} = 3V$, $2V$, and $V_{EE} = -2.5V$



Deliverables:

- Verilog Model
- LIB (best/worst) & LEF models
- GDSII + LVS Spice netlist
- User documentation

Ordering Information:

Ordering Part Number	Technology Options	Conditions	Version
LM-EAMDR-01	180 nm SiGe	0 to 70C	1